COMPUTER SCIENCE **9608/11**

Paper 1 Written Paper **May/June 2018**

MARK SCHEME

Maximum Mark: 75

Published

This mark scheme is published as an aid to teachers and candidates, to indicate the requirements of the examination. It shows the basis on which Examiners were instructed to award marks. It does not indicate the details of the discussions that took place at an Examiners' meeting before marking began, which would have considered the acceptability of alternative answers.

Mark schemes should be read in conjunction with the question paper and the Principal Examiner Report for Teachers.

Cambridge International will not enter into discussions about these mark schemes.

Cambridge International is publishing the mark schemes for the May/June 2018 series for most Cambridge IGCSE™, Cambridge International A and AS Level and Cambridge Pre-U components, and some Cambridge O Level components.

Generic Marking Principles

These general marking principles must be applied by all examiners when marking candidate answers. They should be applied alongside the specific content of the mark scheme or generic level descriptors for a question. Each question paper and mark scheme will also comply with these marking principles.

GENERIC MARKING PRINCIPLE 1:

Marks must be awarded in line with:

- the specific content of the mark scheme or the generic level descriptors for the question
- the specific skills defined in the mark scheme or in the generic level descriptors for the question
- the standard of response required by a candidate as exemplified by the standardisation scripts.

GENERIC MARKING PRINCIPLE 2:

Marks awarded are always **whole marks** (not half marks, or other fractions).

GENERIC MARKING PRINCIPLE 3:

Marks must be awarded **positively**:

- marks are awarded for correct/valid answers, as defined in the mark scheme. However, credit is given for valid answers which go beyond the scope of the syllabus and mark scheme, referring to your Team Leader as appropriate
- marks are awarded when candidates clearly demonstrate what they know and can do
- marks are not deducted for errors
- marks are not deducted for omissions
- answers should only be judged on the quality of spelling, punctuation and grammar when these features are specifically assessed by the question as indicated by the mark scheme. The meaning, however, should be unambiguous.

GENERIC MARKING PRINCIPLE 4:

Rules must be applied consistently e.g. in situations where candidates have not followed instructions or in the application of generic level descriptors.

GENERIC MARKING PRINCIPLE 5:

Marks should be awarded using the full range of marks defined in the mark scheme for the question (however; the use of the full mark range may be limited according to the quality of the candidate responses seen).
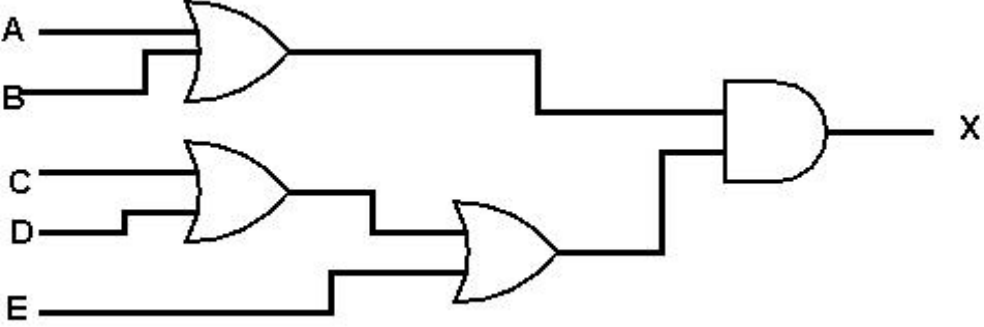

GENERIC MARKING PRINCIPLE 6:

Marks awarded are based solely on the requirements as defined in the mark scheme. Marks should not be awarded with grade thresholds or grade descriptors in mind.

Question	Answer	Marks
1	**1 mark** for a correct line from each communication media, **max 6**	**6**

Question	Answer	Marks
2(a)	**1 mark** per bullet, **max 2** • Made up of pixels • Each pixel has one colour • Colour of each pixel stored as a binary number	**2**
2(b)(i)	**1 mark** per bullet, **max 2** • Each pixel requires only one bit (as there are only two colours) • Black represented by 1 and white by 0 (or vice versa) • Bits are stored for each pixel in sequence • 11111 01010 01010 01010 01010	**2**
2(b)(ii)	**1 mark** for the explanation • Stores the colour and the number of times it occurs **1 mark** for example from • An example from the bitmap given e.g. B5, W1, B1 and so on	**2**
2(c)	**1 mark** per bullet • Number of pixels 500*1000 (= 500 000) • 35 colours require 6 bits per pixel • Number of bytes (500 000 * 6) / 8 = 3 000 000 / 8 (= 375 000) • **= 375 Kb**	**4**

Question	Answer	Marks
2(d)	**1 mark** per bullet to **max 2** marks per benefit • Can resize it without pixilation • Image is redrawn/recalculated with each adjustment • Smaller file size • Storing points/equations/commands etc., not individual pixels	**4**

Question	Answer	Marks
3(a)(i)	**1 mark** per bullet to **max 3** • Allocates / deallocates RAM to programs/tasks/processes • Keeps track of allocated and free memory locations • Swaps data to and from the hard drive • Handles virtual memory • Paging // segmentation • Memory protection, preventing a process accessing memory not allocated to it	**3**
3(a)(ii)	**1 mark** per bullet to **max 3** • Sets up user accounts • Checks usernames, passwords // Authentication • Implements access rights • <u>Automatic</u> backup • System restore / roll back (to previous stable state)	**3**
3(a)(iii)	**1 mark** per bullet to **max 2** • Device / peripheral management • File management • Process management • Input / output management • Error detection / recovery • Provides a user interface • Facilitates communication between hardware and software / hardware devices	**2**
3(b)	**1 mark** for each correct box ticked <table><tr><th>Programs</th><th>True</th><th>False</th></tr><tr><td>Disk Defragmenter</td><td>✓</td><td></td></tr><tr><td>Word Processor</td><td></td><td>✓</td></tr><tr><td>Library program</td><td></td><td>✓</td></tr><tr><td>Compression Software</td><td>✓</td><td></td></tr></table>	**4**

Question	Answer	Marks
4(a)	1 mark per bullet: • A OR B • C OR D OR E • Final AND 	**3**
4(b)	**1 mark** for each correct pair of rows	**4**

A	B	C	Working space	X
0	0	0		0
0	0	1		1
0	1	0		1
0	1	1		0
1	0	0		1
1	0	1		1
1	1	0		1
1	1	1		1

Question	Answer	Marks
5(a)	**1 mark** per server e.g. • E-mail • Print • Web	**2**
5(b)	**1 mark** for the indicating the statement is false: <table><tr><td>True</td><td>False</td></tr><tr><td></td><td>✓</td></tr></table> **1 mark** per bullet for justification to **max 4**. • Internet is the infrastructure / global collection of networks • World Wide Web is the (multimedia web) pages / content • The World Wide Web is accessed over the Internet • Webpages are written in HTML • HTTP protocol used to transfer web pages • Internet uses IP protocol	**5**
5(c)	**1 mark** per bullet to **max 4** • The <u>browser</u> requests the web page • The web server accesses the page • The web server processes / executes the code • The web server produces the HTML for the web page / generates the web page • The web server returns the web page to the client • The client browser displays this web page	**4**

Question	Answer	Marks
6(a)	**1 mark** for the correct parity **Parity bit** <table><tr><td>**0**</td><td>0</td><td>1</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr></table>	**1**

Question	Answer	Marks
6(b)(i)	**1 mark** for the correct bit circled	**1**

	Parity bit	Data						
	1	1	0	1	0	1	1	1
	1	0	0	0	1	1	1	0
	0	1	0	0	1	0	1	1
	1	1	1	0	1	①	1	1
Parity byte	**1**	**1**	**1**	**1**	**1**	**0**	**0**	**1**

Question	Answer	Marks
6(b)(ii)	**1 mark** for each bullet point • row and column has incorrect parity (odd instead of even) • the intersection identifies the error	**2**
6(c)	**1 mark** per bullet to **max 2** • Errors in an even number of bits (in the same row or column) • Prevents error being identified • Could appear to be correct	**2**
6(d)	**1 mark** for the name and **max 2** marks for description • **Check sum** • A calculation is done on a block of data • The result is transmitted with the data • Calculation repeated at receiving end • Results compared • If different an error has occurred • **Hash total** • Total of several fields of data • Including fields not usually used in calculations • The result is transmitted with the data • Calculation repeated at receiving end • Results compared • If different an error has occurred	**3**

Question	Answer	Marks
7(a)	**1 mark** for each input device to **max 2** e.g. • (Handheld) remote controller • Joystick / Games pad / joypad • Accelerometer • Microphone • Suitable sensor **1 mark** for output e.g. • Motor/vibrator in joystick • Speaker • Screen/monitor /TV	**3**
7(b)(i)	**1 mark** for each difference to **max 2** • RAM is volatile and ROM is non-volatile • RAM can change and ROM (usually) can't be changed • ROM is read only, RAM is read/write	**2**
7(b)(ii)	**1 mark** for example e.g. • Current game • Currently running processes • Current graphics/sound	**1**
7(b)(iii)	**1 mark** for an example e.g. • Start-up instructions / boot program • Kernel of Operating System	**1**

Question	Answer	Marks
8(a)	**1 mark** per bullet to **max 2** • Small piece / word of (fast) memory • Part of the processor • Temporary storage of data • Data is about to be / has been processed	**2**
8(b)(i)	**1 mark** per bullet to **max 2** • Stores / holds data / instruction when fetched from memory • Stores / holds data which is being written to memory • The location accessed is the address held in the Memory Address Register (MAR)	**2**

Question	Answer	Marks

8(b)(ii)	**1 mark** per bullet to **max 2**
	   • Current Instruction Register (CIR)
	   • Memory Address Register (MAR)
	   • Program Counter (PC)
	   • Accumulator (ACC)
	   • Index Register (IX)
	   • Status Register
	   • Interrupt Register
		2

8(c)(i)	135	**1**
8(c)(ii)	87	**1**
8(c)(iii)	−121	**1**